

11	CTS#	Input	Clear to Send Control Input / Handshake signal.
12	CBUS4	I/O	Configurable CBUS I/O Pin. Function of this pin is configured in the device internal EEPROM. Factory Default function is SLEEP#. See CBUS Signal Options, Table 3.
13	CBUS2	I/O	Configurable CBUS I/O Pin. Function of this pin is configured in the device internal EEPROM. Factory Default function is TXDEN. See CBUS Signal Options, Table 3.
14	CBUS3	I/O	Configurable CBUS I/O Pin. Function of this pin is configured in the device internal EEPROM. Factory Default function is PWREN#. See CBUS Signal Options, Table 3.
22	CBUS1	I/O	Configurable CBUS I/O Pin. Function of this pin is configured in the device internal EEPROM. Factory Default function is RXLED#. See CBUS Signal Options, Table 3.
23	CBUS0	I/O	Configurable CBUS I/O Pin. Function of this pin is configured in the device internal EEPROM. Factory Default function is TXLED#. See CBUS Signal Options, Table 3.

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- Complies with 89/336/EEC EMC Directive
- ESD Protection to IEC1000-4-2 (801.2)
- ±8 kV: Contact Discharge
- ±15 kV: Air-Gap Discharge
- ±15 kV: Human Body Model
- Fast Transient Burst (EFT) Immunity (IEC1000-4-4)
- Low EMI Emissions (EN55022)
- Eliminates Costly TransZorbs*
- 460 kbits/s Data Rate Guaranteed
- Single +5 V Power Supply
- Shutdown Mode 1 μW

